Docket No.: 42P4728XD

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

STEPHEN H. GUNTHER, ET AL.

Application No.:

Filed:

METHODS AND APPARATUS FOR THERMAL For:

MANAGEMENT OF AN INTEGRATED CIRCUIT DIE

Art Group: 2863

Examiner: Tung S. Lau

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Divisional Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No. 09/784,255, filed February 14, 2001) and copies of the references were furnished at that time. Accordingly, per 37 CFR §1.98(d)(1) additional copies of those references are not submitted herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

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Date: April 09, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Tamara M. Simpson

Date

Substitute	Substitute for form 1449A/PTO			Complete if Known		
INFORMATION DISCLOSURE			OUDE	Application Number		_
			SURE	Filing Date	April 9, 2004	
STAT	STATEMENT BY APPLICANT			First Named Inventor	Stephen H. Gunther	
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Sheet	1	of	4	Attorney Docket Number	42P4728X D	

			U.S. PATE	NT DOCUMENTS	
Examiner	0.1	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*		Number - Kind Code ² (if known)	or Issue Date MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
	1	US-6,172,611	01-09-2001	Hussain et al.	
	2	US-5,675,297	10-07-1997	Gose et al.	
	3	US-5,256,914	10-26-1993	Booner, James B	
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	6	US-6,137,329	10-24-2000	Kardash	
	7	US-6,336,593	01-08-2002	Bhatnagar	
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	9	US-H562	12-06-1988	Trachier et al.	
	10	US-Re. 32,960	06-20-1989	Levine	
	11	US-Re. 34,789	11-15-1994	Premerlani	
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	17	US-4,789,819	12-16-1988	Nelson	
	18	US-4,799,176	01-14-1989	Cacciatore	
	19	US-4,807,144	07-25-1999	Day	
	20	US-4,903,106	02-20-1990	Fukunaga et al.	

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Initials*	No.1	Country Code ³ - Number ⁴ - Kind Code ⁸ f known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	

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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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INICOL	RMATION	DISCI OS	LIDE	Application Number		
INFOI	RIVIATION	DISCLUS	UKE	Filing Date	April 9, 2004	
STATEMENT BY APPLICANT				First Named Inventor	Stephen H. Gunther	
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_			U.S. PATE	ENT DOCUMENTS	
Examiner Initials*	Cite No.'	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	21	US-4,924,112	05-08-1990	Anderson et al.	
	22	US-4,935,864	06-19-1990	Schimidt et al.	
	23	US-5,008,771	04-16-1991	Palara	
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	27	US-5,085,526	02-04-1992	Sawtell et al.	
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	29	US-5,105,366	04-14-1992	Becky	
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	36	US-5,283,631	02-01-1994	Koerner et al.	
	37	US-5,287,292	02-15-1994	Kenny et al.	
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Initials*	No.1	Country Code ³ - Number ⁴ - Kind Code ⁶ (f known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear		
	41	0523736A1	01-20-1993	EP			
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Complete if Known		
Application Number		
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Art Unit	2863	
Examiner Name	Tung S. Lau	
Attorney Docket Number	42P4728YD	

			U.S. PATE	NT DOCUMENTS	
Examiner Initials*	Cite No.¹	Document Number Number - Kind Code² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	42	US-5,359,236	10-25-1994	Giordano et al.	
	43	US-5,422,832	06-06-1995	Moyal	
	44	US-5,440,305	08-08-1995	Signore et al.	
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Examiner	Cite	Foreign Patent Document			Pages, Columns, Lines,	
Initials*	No.¹	Country Code ² - Number ⁴ - Kind Code ⁸ f known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
				44-y1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-		

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		1 1		Examiner Name	Tung S. Lau
Sheet	4	of	4	Attorney Docket Number	42P4728X C

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	48	SHU-YUAN CHIN & CHUNG-YU WU. " A New Type of Curvature-Compensated CMOS Bandgap Voltage References', VLSITSA, 1991, Pages 398-402.	
	49	MARCO FERRO, FRANCO SALERNO & RINALDO CASTELLO, "A Floating CMOS Bandgap Voltage References for Differential Applications". IEEE: Journal of Solid-State	
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	51	HECTOR SANCHEZ, ET AL. "Thermal Management System for Hign Performance PowerPC Microprocessors", IEEE, COMPCON '97, February 1997, Pages 325-330.	
	52	PHILLIP E. ALLEN & DOUGLAS R. HOLBERG, "CMOS Analog Circuit Design", HRW, Pages 529-551	
	53	"Low Power, Programmable Temperature Controller", Rev. C, Analog Devices, Inc., 1995 Pages 1-16.	

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